



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Powelson, J.
David K. Warland

Serial No. : 09/496,898 Group Art Unit : 2653
Filed : February 02, 2000 Examiner : Huber, Paul W.
For : Write Compensation for ~~a~~ Multi-
Level Data Storage System Atty Docket : P011/1496.00373 / 03-2430

I hereby certify that this correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450, on the date below:

Manu Kashyap

2/17/05

Date Signature

SUBMISSION OF FORMAL DRAWINGS PURSUANT TO 37 C.F.R. §1.85

Official Draftsman

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Sir:

Applicant hereby substitutes the enclosed formal drawings for those presently in the above referenced application.

LSI Logic Corporation
1621 Barber Lane, MS D-106
Milipitas, CA 95035
408-433-7475

Date: 2/17/2005

Respectfully submitted,

Henry Groth

Reg. No. 39,696